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Melear, C.;

Southcon/96. Conference Record , 25-27 June 1996

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2 A VLSI processor-switch for a dual IEEE-796 bus with shared and dual memories

Opsommer, J.; D'Hollander, E.H.;

CompEuro '89., 'VLSI and Computer Peripherals. VLSI and Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks', Proceedings. , 8-1989

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3 Asynchronous macrocell interconnect using MARBLE

Bainbridge, W.J.; Furber, S.B.;

Advanced Research in Asynchronous Circuits and Systems, 1998. Proceedings. Fourth International Symposium on , 30 March-2 April 1998

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4 Introducing Tobus: the system bus in the TRON architecture

Sakamura, K.; Sano, R.; Honma, K.;
Micro, IEEE , Volume: 8 Issue: 2 , April 1988
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